



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
M.Tech. in Embedded System

SEMESTER - II (2021-2023)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	MTES201	Advanced Microcontroller	2	1	2	4	60	20	20	30	20	150
2	MTDC202	Advanced Digital Signal Processing	3	0	2	4	60	20	20	30	20	150
3	MTES202	Real Time Operating Systems	3	0	0	3	60	20	20	0	0	100
4	MTRM301	Research Methodology in Engineering	3	1	0	4	60	20	20	0	0	100
5	-	Elective II	3	0	0	3	60	20	20	0	0	100
6	-	Generic Elective	3	0	0	3	60	20	20	0	0	100
TOTAL			17	2	4	21	360	120	120	60	40	700

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

***Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**

Elective II		
S. No.	Course Code	Course Name
1	MTES113	HDL Fundamentals
2	MTES123	Robotics and Machine Vision
3	MTDC215	Digital Image Processing

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